

Abstract of the Disclosure

5 A graphics integrated circuit chip is used in a set-top box
for controlling a television display. The graphics chip
processes analog video input, digital video input, a graphics
input and an audio input simultaneously. The chip uses window
descriptors to describe logical surfaces, or windows, of
graphics information to be displayed on the screen. The chip
10 incorporates a unified memory architecture that provides a high
level of system performance while conserving memory bandwidth
and chip size. Video and graphics scaling capabilities as well
as anti-flutter filtering capability are provided.

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